Title (en)

ARRANGEMENT FOR DECODING SIGNALS

Publication

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Application

EP 88106346 A 19880421

Priority

DE 3716285 A 19870515

Abstract (en)

[origin: EP0290829A2] In an arrangement for decoding signals which are received in addition to other signals as a modulation of radio waves and whose respective frequency constitutes a specific item of information, a time basis signal of constant frequency is generated from which a reference signal with a controllable frequency position and phase position is derived with the aid of a microprocessor. An output signal is derived if there is a coincidence of the reference signal with the received signal with respect to phase and frequency. The time basis signal can preferably be derived from the instruction frequency of the microprocessor by running through program loops of identical length at least in the timing means. A register which is incremented by changeable values serves for deriving the reference frequency. In the case of two superimposed signals, the one is evaluated with respect to frequency and the other with respect to the pulse duty factor of a binary signal. <IMAGE>

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