

Title (en)
PROCESS FOR THE EXAMINATION OF COMPONENTS

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Application
EP 88109462 A 19880614

Priority
DE 3720219 A 19870617

Abstract (en)
[origin: EP0296461A2] The invention relates to a process for examining components being produced or in use and which have internal defects with the aid of ultrasonic signals, in particular in the case of thick-walled components such as forgings. The ultrasonic probe is guided along a line over the surface of the component, while a synthetic aperture is assigned electronically in a computer to the ultrasonic signals and the signals are subsequently represented in a memory. The received signals are averaged and further processed depending on the density of test data for the purposes of image reconstruction or pattern recognition, after which in the case of components which are still being processed the deformation conditions are set such that a tolerable defect measure used for comparison is undershot, while in the case of components already in use their exchange is undertaken upon overshooting of a tolerable defect measure. <IMAGE>

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Citation (search report)
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