

Title (en)

VOLTAGE SOURCE CIRCUIT ARRANGEMENT WITH PREDETERMINED VALUES OF THE SOURCE VOLTAGE AND THE INTERNAL RESISTANCES

Publication

EP 0301284 B1 19930407 (DE)

Application

EP 88110804 A 19880706

Priority

DE 3725348 A 19870730

Abstract (en)

[origin: US4878009A] A voltage source with preset values of the source voltage and the internal resistance is simulated by a computing circuit (25, 26; 35, 36; 47) which calculates a reference parameter (SUO; SIO) for a current or voltage regulator (20; 30; 40) that forms the output of the voltage source. The reference parameter (SUO; SIO) corresponds to the output current (IO) or the output voltage (UO) and is obtained from a measured parameter (MUO; MIO) and the input parameters (SU; SR) which correspond to the values to be set. The measured parameter (MUO; MIO) is derived from the output voltage (UO) or the output current (IO). When using this simulation circuit, separate high load resistors and mechanical switching contacts are unnecessary and the input parameters (SU; SR) can be set with analog switches.

IPC 1-7

G05F 1/575

IPC 8 full level

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CPC (source: EP US)

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Citation (examination)

- HANDBOOK OF INTEGRATED-CIRCUIT OPERATIONAL AMPLIFIERS, G.B.RUTKOWSKI, Prentice-Hall, Englewood Cliffs, New Jersey, US, 1975
- DATA CONVERSION SEMINAR, Juli 1984, Analog Devices Inc., US

Cited by

EP0837382A1

Designated contracting state (EPC)

DE ES FR GB IT NL SE

DOCDB simple family (publication)

EP 0301284 A2 19890201; EP 0301284 A3 19890614; EP 0301284 B1 19930407; DE 3725348 A1 19890209; DE 3725348 C2 19910314; DE 3880036 D1 19930513; ES 2041743 T3 19931201; US 4878009 A 19891031

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