

Title (en)
COIN VALIDATORS

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EP 0308997 B1 19930922 (EN)

Application
EP 88117268 A 19841105

Priority
• GB 8329533 A 19831104
• GB 8405721 A 19840305

Abstract (en)
[origin: WO8502047A1] A coin validator comprises a microprocessor (4) which is periodically activated to check the output of an arrival sensing circuit (6) to determine whether a coin has arrived. The circuit (6) includes a printed circuit inductance (10) and produces an output signal the frequency of which alters when a coin arrives and is used to indicate coin diameter. The circuit (6) is designed so that output amplitude variations are minimised. Arrival of a coin causes powering-up of a material/thickness sensing circuit (8). In one embodiment both the frequency and amplitude of the output signal from this circuit are used to determine the denomination of the inserted coin. The amplitude profile may be used to determine when to measure frequency, and which of a plurality of successive amplitude measurements are to be combined to provide an averaged amplitude value. A counter (24) is used for frequency and amplitude measurement, and for periodically activating the microprocessor (4). Coins are rejected if they travel too quickly through a testing station of the validator. The validator is operable in a test mode to indicate, in response to an inserted coin, how the sensing circuits (6, 8) should be adjusted to provide optimum operation.

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CPC (source: EP)
G07D 5/02 (2013.01); **G07D 5/08** (2013.01)

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US 3797628 A 19740319 - FOUGERE G, et al

Cited by
EP0470587A3; GB2221332B

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WO 8502047 A1 19850509; DE 3485866 D1 19920917; DE 3485866 T2 19921210; DE 3486213 D1 19931028; DE 3486213 T2 19940113; EP 0146251 A1 19850626; EP 0146251 B1 19920812; EP 0308996 A2 19890329; EP 0308996 A3 19890517; EP 0308997 A2 19890329; EP 0308997 A3 19890517; EP 0308997 B1 19930922; GB 2151062 A 19850710; GB 2151062 B 19880629; GB 8427935 D0 19841212; HK 74297 A 19970613; SG 32491 G 19910621

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