

Title (en)
AMPLIFIER ARRANGEMENT

Publication
EP 0309063 B1 19921209 (EN)

Application
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Priority
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Abstract (en)
[origin: EP0309063A1] The first (T1) and the second (T2) output transistor of an amplifier arrangement are push-pull driven by means of a drive circuit (10) having two transistors (T11, T12) which are each loaded by a current source (T13, T14). Currents which are a measure of the currents flowing through the first (T1) and the second (T2) output transistor are generated by a first (20) and second (30) current measuring means. These currents are applied to a negative feedback means (40) which controls the current intensity of the current sources (T13, T14) in such a way that the harmonic mean value of the currents flowing through the first (T1) and the second (T2) output transistor is substantially equal to a reference value.

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IPC 8 full level
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CPC (source: EP KR US)
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Cited by
EP0762635A3; EP0669709A1; US6150853A; EP0425838A1; US5083046A; EP0415503A1; FR2651623A1; EP0843409A1; US5973555A; EP0840444A1; US5920229A; US7012465B2; WO03015261A1

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