

Title (en)

PLA driver with reconfigurable drive.

Title (de)

PLA-Treiberschaltung mit umstellbarer Steuerung.

Title (fr)

Circuit d'attaque pour PLA à commande reconfigurable.

Publication

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Application

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Priority

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Abstract (en)

In accordance with the teachings of this invention, a novel PLA row driver circuit is provided which utilizes a minimum number of components, thereby minimizing integrated circuit surface area, and thus reducing cost, and minimizing stray capacitance, thereby increasing speed of operation. Furthermore, in accordance with the teachings of this invention, a circuit (Q2, Q3, Q4, Q5) is provided which, while utilizing a minimum number of components, provides a first VOL level to the row line (RW1) during normal operation of the device, and a second, higher VOL level to the row line (RW1) during programming.

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