

Title (en)

PLA driver with reconfigurable drive.

Title (de)

PLA-Treiberschaltung mit umstellbarer Steuerung.

Title (fr)

Circuit d'attaque pour PLA à commande reconfigurable.

Publication

**EP 0309357 A2 19890329 (EN)**

Application

**EP 88402398 A 19880923**

Priority

US 10121087 A 19870925

Abstract (en)

In accordance with the teachings of this invention, a novel PLA row driver circuit is provided which utilizes a minimum number of components, thereby minimizing integrated circuit surface area, and thus reducing cost, and minimizing stray capacitance, thereby increasing speed of operation. Furthermore, in accordance with the teachings of this invention, a circuit (Q2, Q3, Q4, Q5) is provided which, while utilizing a minimum number of components, provides a first VOL level to the row line (RW1) during normal operation of the device, and a second, higher VOL level to the row line (RW1) during programming.

IPC 1-7

**H03K 19/177**

IPC 8 full level

**G06F 7/00** (2006.01); **H03K 19/177** (2006.01)

CPC (source: EP US)

**H03K 19/17712** (2013.01 - EP US)

Designated contracting state (EPC)

AT BE CH DE ES FR GB GR IT LI LU NL SE

DOCDB simple family (publication)

**EP 0309357 A2 19890329; EP 0309357 A3 19900314; EP 0309357 B1 19940427**; AT E105121 T1 19940515; CA 1288137 C 19910827; DE 3889273 D1 19940601; DE 3889273 T2 19941208; JP H021618 A 19900105; US 4859874 A 19890822

DOCDB simple family (application)

**EP 88402398 A 19880923**; AT 88402398 T 19880923; CA 578232 A 19880923; DE 3889273 T 19880923; JP 23907788 A 19880926; US 10121087 A 19870925