

Title (en)

METHOD OF CONTROLLING A MATRIX DISPLAY SCREEN, AND DEVICE FOR CARRYING OUT THIS METHOD

Publication

EP 0329528 B1 19930804 (FR)

Application

EP 89400355 A 19890208

Priority

FR 8801741 A 19880215

Abstract (en)

[origin: EP0329528A1] Method of controlling a matrix display screen permitting adjustment of its contrast in the case of a liquid-crystal screen and its brightness in the case of a microchip fluorescent screen, consisting in periodically applying, to row conductors, addressing signals VI having, during a certain period, a value Vmax in absolute value, and in applying control signals to column conductors. There are applied to the row conductors addressing signals for which the periods during which they have a value Vmax partially overlap, for two consecutive rows. <??>Device for carrying out this method. Application to the construction of displays for viewing still or moving images. <IMAGE>

IPC 1-7

G09G 3/36

IPC 8 full level

G02F 1/133 (2006.01); **G09G 3/22** (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP US)

G09G 3/22 (2013.01 - EP US); **G09G 3/3611** (2013.01 - EP US); **G09G 2310/0205** (2013.01 - EP US)

Cited by

FR2749431A1

Designated contracting state (EPC)

CH DE FR GB IT LI NL

DOCDB simple family (publication)

EP 0329528 A1 19890823; EP 0329528 B1 19930804; EP 0329528 B2 19970903; DE 68907942 D1 19930909; DE 68907942 T2 19940210;
DE 68907942 T3 19980326; FR 2627308 A1 19890818; FR 2627308 B1 19900601; JP H025088 A 19900109; US 5032832 A 19910716

DOCDB simple family (application)

EP 89400355 A 19890208; DE 68907942 T 19890208; FR 8801741 A 19880215; JP 3123789 A 19890213; US 31034089 A 19890214