

Title (en)

METHOD FOR ATTACHING SEMICONDUCTOR COMPONENTS TO SUBSTRATES, AND ARRANGEMENT FOR CARRYING IT OUT

Publication

EP 0330896 A3 19910109 (DE)

Application

EP 89102448 A 19890213

Priority

DE 3806980 A 19880303

Abstract (en)

[origin: EP0330896A2] A diffusion welding process is provided for attaching semiconductor components (3), in particular power semiconductors, to substrates (1). In the process, the surfaces to be joined, which are provided with a noble metal contacting layer, are pressed together at at least 500 kgf/cm² at a moderate temperature of about 150 to 250 DEG C. Components (3) having a structured upper side can be joined to substrates if they are placed, together with a body (17, 18) of elastically deformable material, for example silicone rubber, in a receiving chamber (15a, 15b, 21) which is sealed by a movable die (21) and which transmits the contact pressure. On reaching the contact pressure, the remaining interior space of the receiving chamber is completely filled by the deformable body (17, 18). <IMAGE>

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H01L 21/603

IPC 8 full level

B23K 20/00 (2006.01); **H01L 21/60** (2006.01); **H01L 21/603** (2006.01)

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H01L 2924/1301 (2013.01 - EP US); **H01L 2924/351** (2013.01 - EP US)

Citation (search report)

- [Y] US 3228104 A 19660111 - REIMER EMEIS
- [Y] EP 0242626 B1 19910612
- [A] GB 2067117 A 19810722 - SECR DEFENCE
- [A] PATENT ABSTRACTS OF JAPAN, Band 6, Nr. 55 (E-101)[933], 10. April 1982; & JP-A-56 167 352 (MITSUBISHI) 23-12-1981

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