

Title (en)

Scrambling/descrambling method using a memory for storing initial pseudo-random bit patterns respectively for submultiframes.

Title (de)

Verwürflungs/Entwürflungsverfahren mit einem Speicher zur Speicherung von anfänglichen Pseudozufallsbitmustern für jeweilige Mehrfachunterrahmen.

Title (fr)

Procédé d'embrouillage et de désembrouillage utilisant une mémoire destinée à l'enregistrement de motifs initiaux de bits pseudo-aléatoires pour les sous-multiframes respectives.

Publication

**EP 0331115 A2 19890906 (EN)**

Application

**EP 89103510 A 19890228**

Priority

JP 4790288 A 19880301

Abstract (en)

In a communications system, a multiframe data signal is sent from a transmit end to a receive end of the system. The multiframe is composed of a series of submultiframes each containing a series of frames. Each frame in turn comprises a sync word for establishing the start timing of the frame, an identifier for uniquely identifying the submultiframe in which the frame is contained, and a specified number of data bits. A pseudo-random scrambling bit pattern, which repeats at intervals of multiframe and varies from one pattern to another at frame intervals, is generated as a scrambling bit pattern with which the data bits of each frame are scrambled prior to transmission. At the receive end, the sync word of each frame is detected to establish frame synchronization. A memory stores initial bit patterns each of which is identical to the pattern which was generated at the beginning of each submultiframe. The identifier is detected when frame sync is established and applied as a read address to the memory. The initial bit pattern of the submultiframe which is next to a one in which the establishment of frame sync occurred is read out of the memory into a pseudo-random sequence generator. The latter is activated at the beginning of the next submultiframe to generate a pseudo-random sequence, which successively varies at frame intervals and repeats the same pattern at multiframe intervals, for descrambling received data bits.

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**H04L 7/04**; **H04L 25/49**

IPC 8 full level

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