

Title (en)

STABILIZED GENERATOR FOR THE DELIVERY OF A THRESHOLD VOLTAGE FOR A MOS TRANSISTOR

Publication

**EP 0332548 B1 19930714 (FR)**

Application

**EP 89420084 A 19890308**

Priority

FR 8803751 A 19880309

Abstract (en)

[origin: EP0332548A1] Stabilised generator included within a MOS integrated circuit in order to deliver a bias voltage to a first comparator (1) connected to a first inverter (2) intended to deliver a voltage equal to a MOS transistor (M) threshold voltage when the input voltages thereof are identical, including a second comparator (11) whose inputs are together connected to a reference voltage and a second inverter (12), which are identical to the first ones, and a third inverter (13) receiving the output from the second one and whose output (14) is connected to the bias inputs of the comparators (1, 11), this third inverter being rated so that its threshold voltage is identical with that of an MOS transistor. <IMAGE>

IPC 1-7

**G05F 3/24**

IPC 8 full level

**H01L 21/8234** (2006.01); **G05F 3/24** (2006.01); **H01L 27/088** (2006.01); **H03K 5/08** (2006.01); **H03K 17/30** (2006.01)

CPC (source: EP KR US)

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**EP 0332548 A1 19890913**; **EP 0332548 B1 19930714**; DE 68907504 D1 19930819; DE 68907504 T2 19940105; FR 2628547 A1 19890915; FR 2628547 B1 19901228; JP H0210917 A 19900116; KR 890015102 A 19891028; US 4954728 A 19900904

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