

Title (en)
IMPROVEMENTS IN GRAPHIC DISPLAY SYSTEMS

Publication
EP 0337752 A3 19911121 (EN)

Application
EP 89303602 A 19890412

Priority
JP 9191388 A 19880415

Abstract (en)
[origin: EP0337752A2] A graphic display system in which a raster driven display screen (24) receives input signals from a storage device (10). Means (12) are provided to define the outline of a portion of an image and then to generate a mask (14) which is overlaid on the signals in the storage device so that only the signals representing the portion of the image inside the outline are displayed on the screen.

IPC 1-7
G09G 1/16

IPC 8 full level
G06F 3/153 (2006.01); **G06T 3/00** (2006.01); **G06T 11/00** (2006.01); **G09G 5/36** (2006.01); **G09G 5/39** (2006.01)

CPC (source: EP KR)
G03G 15/00 (2013.01 - KR); **G09G 1/16** (2013.01 - KR); **G09G 5/36** (2013.01 - EP)

Citation (search report)
• [A] EP 0145821 A1 19850626 - IBM [US]
• [A] IBM TECHNICAL DISCLOSURE BULLETIN. vol. 28, no. 6, November 1985, NEW YORK US pages 2721 - 2724; 'GRAPHIC BIT-BLT COPY UNDER MASK '
• [A] JEE JOURNAL OF ELECTRONIC ENGINEERING. vol. 24, no. 251, November 1987, TOKYO JP pages 28 - 31; MASAYOSHI NAKANE: 'VIDEO RAM CHIPS DESIGNED FOR COMPUTER GRAPHICS '

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CN1058797C; US8769406B2

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0337752 A2 19891018; EP 0337752 A3 19911121; EP 0337752 B1 19950104; BR 8901475 A 19891114; CN 1011092 B 19910102; CN 1037975 A 19891213; DE 68920353 D1 19950216; DE 68920353 T2 19950713; JP H01270176 A 19891027; KR 890016434 A 19891129; KR 930004023 B1 19930519

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