

Title (en)
Multiplexer circuit.

Title (de)
Multiplexstromkreis.

Title (fr)
Circuit multiplex.

Publication
EP 0341929 A2 19891115 (EN)

Application
EP 89304573 A 19890505

Priority
• GB 8811458 A 19880513
• GB 8830397 A 19881230

Abstract (en)
A multiplexer circuit for effecting in successive phases of operation thereof actuation of selected devices of respective groups of devices of a series of capacitance actuated devices has a series of parallel electrical paths to which the respective devices are connected. A signal generator is connected across said paths and two capacitors of each device are connected between the path of the associated device and the respective paths on opposite sides of the path of the associated device. First and second switching means are disposed in each path and are closed by respective logic signals applied thereto so that when the first and second switching means of one path are respectively closed and open and the first and second switching means of each of the paths on respective opposite sides of that one path are respectively open and closed, charging of the capacitors connected to that one path takes place and when thereafter the first and second switching means of that one path are respectively open and closed discharge of the capacitors connected to that one path takes place.

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B41J 3/04; **H03K 17/687**; **H03K 17/693**

IPC 8 full level
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CPC (source: EP)
B41J 2/085 (2013.01); **B41J 2202/10** (2013.01)

Cited by
FR2705279A1; US5463414A; EP0779151A3; US6113209A; SG83183A1; US6991323B1; WO9222429A1

Designated contracting state (EPC)
AT CH DE ES FR GB GR IT LI NL SE

DOCDB simple family (publication)
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