

Title (en)
MICROPROGRAMMABLE LANGUAGE EMULATION SYSTEM

Publication
EP 0343171 A4 19910130 (EN)

Application
EP 88900933 A 19871228

Priority
US 8703444 W 19871228

Abstract (en)
[origin: WO8805190A1] A data processing system executes a high level computer language program by encoding statements of the program into variable length tokens and then executing the tokens. Each token is a variable length bit field having a value representative of a semantic element of a program statement and a length representative of the context of the semantic element. A single very large scale integrated circuit (VLSI) processor (40) is microprogrammed to execute the encoded program in a pipeline manner and may be used in conjunction with a host computer (20), such as an IBM PC AT or equivalent system.

IPC 1-7
G06F 9/34; G06F 9/44

IPC 8 full level
G06F 9/455 (2006.01); **G06F 9/34** (2006.01); **G06F 9/44** (2006.01); **H01L 21/28** (2006.01); **H01L 21/443** (2006.01); **H01L 31/0224** (2006.01); **H01L 31/072** (2006.01); **H01L 31/10** (2006.01)

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Citation (search report)
• [YP] MICROPROCESSING AND MICROPROGRAMMING, vol. 21, nos. 1-5, August 1987, pages 593-602, Amsterdam, NL; E.H. DEBAERE: "A language coprocessor for the interpretation of threaded code"
• [Y] PROCEEDINGS OF THE NATIONAL ELECTRONICS CONFERENCE, Oakbrook, Illinois, 7th - 9th December 1970, pages 119-124; O.R. PARDO: "Microprogramming by the computer design language"
• [Y] THE JOURNAL OF FORTH APPLICATION AND RESEARCH, vol. 2, no. 1, 1984, pages 49-63; J.C. VAUGHAN et al.: "The design of a forth computer"
• [Y] IEEE JOURNAL OF SOLID STATE CIRCUITS, vol. SC-21, no. 5, October 1986, pages 741-749; J.M. PENDLETON et al.: "A 32-bit microprocessor for smalltalk"
• [Y] OPTICAL ENGINEERING, vol. 25, no. 11, November 1986, pages 1186-1192; S. KRUEGER et al.: "Artificial intelligence hardware architectures for the space station era: the Texas instruments explorer and compact LISP machine"
• [Y] PROCEEDINGS OF THE 19TH IEEE COMPUTER SOCIETY INTERNATIONAL CONFERENCE, Washington, D.C., 4th - 7th September 1979, pages 115-119; R.M. HIDINGER et al.: "A direct execution processor"
• See references of WO 8805190A1

Designated contracting state (EPC)
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