

Title (en)

An IC chip for an analog electronic watch.

Title (de)

Integriertes Schaltungschip für elektronische Analoguhr.

Title (fr)

Circuit intégré pour montre analogique électronique.

Publication

EP 0347249 B1 19940601 (EN)

Application

EP 89306144 A 19890616

Priority

JP 15087388 A 19880617

Abstract (en)

[origin: EP0347249A2] An IC chip (20) for an analog electronic watch has a core CPU (201), a program memory (202) for storing software for actuating the core CPU, and a motor drive controlling circuit (212) for controlling a plurality of step motors according to commands of the software. An analog electronic watch having such an IC chip also has a plurality of step motors, and at least one gear train mechanism to couple each step motor to a rotatable indicator hand. Drive of the step motors can be controlled arbitrarily according to the software stored in the program memory.

IPC 1-7

G04C 3/14

IPC 8 full level

G04C 3/14 (2006.01); **G04C 3/00** (2006.01); **G04C 23/08** (2006.01); **G04F 8/02** (2006.01); **G04G 99/00** (2010.01)

CPC (source: EP KR)

G04C 3/14 (2013.01 - EP); **G04C 9/00** (2013.01 - KR); **G04G 9/00** (2013.01 - KR); **G04G 99/00** (2013.01 - EP)

Citation (examination)

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- EP 0250232 A2 19871223 - SEIKO INSTR INC [JP]
- EP 0253227 A1 19880120 - EM MICROELECTRONIC MARIN SA [CH]
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- EP 0267440 A1 19880518 - EBAUCHESFABRIK ETA AG [CH]
- WO 8805184 A1 19880714 - MOTOROLA INC [US]

Cited by

FR2685502A1

Designated contracting state (EPC)

CH DE FR GB LI

DOCDB simple family (publication)

EP 0347249 A2 19891220; EP 0347249 A3 19910320; EP 0347249 B1 19940601; CN 1025458 C 19940713; CN 1038708 A 19900110; DE 68915618 D1 19940707; DE 68915618 T2 19940915; HK 107097 A 19970822; JP 2000046967 A 20000218; JP 3019324 B2 20000313; JP 3259718 B2 20020225; JP H0277679 A 19900316; JP H11194178 A 19990721; KR 900000740 A 19900131; KR 940008709 B1 19940926

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