

Title (en)
PARALLEL NETWORKING ARCHITECTURE

Publication
EP 0353249 A4 19920108 (EN)

Application
EP 88904083 A 19880401

Priority
US 3488187 A 19870406

Abstract (en)
[origin: WO8808167A1] A new digital data transmission architecture is disclosed utilizing a networking concept which can be used to interconnect in parallel, contemporaneously, the many resources and facilities which are normally interconnected with one or more host computer systems (24, 25). Because the adapter logic (20) and matrix distribution switch (30) disclosed serve solely to interconnect a requesting device (29, 35) with the destination receiving device (29, 35), the invention can be used in any data transmission environment where any one of three or more data transmission devices (29, 35) are configured to transmit to any of the interconnected devices (29, 35). Since the N x N switch (30) is configured to establish a separate path for each device requesting a transmission, independent parallel processing is achieved.

IPC 1-7
G06F 15/56; H01H 67/00; H04Q 11/00

IPC 8 full level
G06F 13/40 (2006.01); **G06F 15/16** (2006.01); **G06F 15/173** (2006.01); **G06F 11/08** (2006.01)

CPC (source: EP)
G06F 13/4022 (2013.01); **G06F 15/17375** (2013.01); **G06F 11/08** (2013.01)

Citation (search report)

- No further relevant documents have been disclosed.
- See references of WO 8808167A1

Designated contracting state (EPC)
AT BE CH DE FR GB IT LI LU NL SE

DOCDB simple family (publication)
WO 8808167 A1 19881020; EP 0353249 A1 19900207; EP 0353249 A4 19920108; JP H02503366 A 19901011

DOCDB simple family (application)
US 8801184 W 19880401; EP 88904083 A 19880401; JP 50396488 A 19880401