

Title (en)
PARALLEL NETWORKING ARCHITECTURE

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Application
EP 88904083 A 19880401

Priority
US 3488187 A 19870406

Abstract (en)
[origin: WO8808167A1] A new digital data transmission architecture is disclosed utilizing a networking concept which can be used to interconnect in parallel, contemporaneously, the many resources and facilities which are normally interconnected with one or more host computer systems (24, 25). Because the adapter logic (20) and matrix distribution switch (30) disclosed serve solely to interconnect a requesting device (29, 35) with the destination receiving device (29, 35), the invention can be used in any data transmission environment where any one of three or more data transmission devices (29, 35) are configured to transmit to any of the interconnected devices (29, 35). Since the N x N switch (30) is configured to establish a separate path for each device requesting a transmission, independent parallel processing is achieved.

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Citation (search report)

- No further relevant documents have been disclosed.
- See references of WO 8808167A1

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