

Title (en)

METHOD FOR THE VERY FAST CONTROL OF AN AC PLASMA PANEL WITH A COPLANAR SUPPORT BY SEMI-SELECTIVE ADDRESSING AND SELECTIVE ADDRESSING

Publication

EP 0356313 B1 19930714 (FR)

Application

EP 89402282 A 19890811

Priority

FR 8811248 A 19880826

Abstract (en)

[origin: EP0356313A1] The invention relates to a method for controlling the pixels (PX1 to PX32) of a plasma panel (1) by selective addressing and by semi-selective addressing, and applies in the cases where a pixel (PX1 to PX32) is defined at the meeting point of a column electrode (X1 to X4) with a pair (P1 to P8) of service electrodes. <??>The method of the invention permits namely a reduced image cycle time to be obtained. For this purpose, according to a characteristic of the invention, the method consists in simultaneously controlling certain pixels by semi-selective addressing and other pixels by selective addressing. <IMAGE>

IPC 1-7

G09G 3/28

IPC 8 full level

G09G 3/294 (2013.01); **G09G 3/292** (2013.01); **G09G 3/293** (2013.01); **G09G 3/298** (2013.01)

CPC (source: EP US)

G09G 3/2948 (2013.01 - EP US); **G09G 3/2927** (2013.01 - EP US); **G09G 3/293** (2013.01 - EP US); **G09G 3/298** (2013.01 - EP US)

Cited by

US6229504B1

Designated contracting state (EPC)

DE FR GB NL

DOCDB simple family (publication)

EP 0356313 A1 19900228; EP 0356313 B1 19930714; DE 68907533 D1 19930819; DE 68907533 T2 19940203; FR 2635902 A1 19900302; FR 2635902 B1 19901012; US 5030888 A 19910709

DOCDB simple family (application)

EP 89402282 A 19890811; DE 68907533 T 19890811; FR 8811248 A 19880826; US 39663889 A 19890822