Title (en)

Device for the processing and wireless transmission of measured values.

Title (de)

Einrichtung zum Verarbeiten und drahtlosen Senden von Messwerten.

Title (fr)

Dispositif pour le traitement et l'émission sans fil de valeurs de mesure.

Publication

EP 0361024 A2 19900404 (DE)

Application

EP 89114037 A 19890729

Priority

DE 3832985 A 19880929

Abstract (en)

[origin: JPH02109199A] PURPOSE: To process and transmit the measured value which has operation certaintity by radio by transferring high-frequency pulses from a crystal oscillator to a transmitter when a signal corresponding to the measured value of a P/S converter is supplied to the input terminal of a logic element. CONSTITUTION: A temperature measured value, etc., is processed by a microcomputer 40 and transferred to the transmitter 4 for transmission through the P/S converter 62. At this time, the converter 62 outputs pulses to an output terminal, and as its pulse length and interval, measured values processed by the computer 40 are coded and continuously displayed. The pulses are sent to the input terminal 66 of the logic element 42 through a conductor 65. An AND element is used as the element for positive logic and only when an input terminal 66 has logic 1, i.e., when a signal corresponding to the measured values is supplied, high-frequency pulses from the crystal oscillator 44 are sent from the input terminal 68 to the output terminal 69, so that data are sent out to the transmitter 4.

Abstract (de)

Analoge Meßwerte werden über einen Analog-Digital-Wandler (50) und/oder digitale Meßwerte werden über einen Timer (60) in einem Mikrocomputer (40) verarbeitet und über einen Parallel-Seriell-Wandler (62) einem Logikbaustein (42) zugeführt. Dem Logikbaustein (42) werden außerdem an einem weiteren Eingang Hochfreguenz-Pulse von einem Quarzoszillator (44) zugeführt. Der Logikbaustein (42) überträgt die Hochfrequenz-Pulse des Quarzoszillators (44) jeweils dann auf einen Sender (4) zur drahtlosen Meßwertübertragung, solange der Parallel-Seriell-Wandler (62) einem anderen Eingang des Logikbausteines (42) Pulse zuführt, deren Länge und Abstände voneinander von den gemessenen Meßwerten abhängig sind.

IPC 1-7

G08C 17/00; G08C 19/28

IPC 8 full level

G08C 15/06 (2006.01); **G08C 17/00** (2006.01); **G08C 19/28** (2006.01); **H04B 1/04** (2006.01)

CPC (source: EP US)

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DE ES FR GB IT SE

DOCDB simple family (publication)

EP 0361024 A2 19900404; **EP 0361024 A3 19900509**; **EP 0361024 B1 19940209**; DE 3832985 A1 19900405; DE 58906936 D1 19940324; JP H02109199 A 19900420; US 5041827 A 19910820

DOCDB simple family (application)

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