

Title (en)

METHOD AND APPARATUS FOR COMMUNICATING DATA BETWEEN MULTIPLE TASKS IN DATA PROCESSING SYSTEMS

Publication

EP 0361176 A3 19920826 (EN)

Application

EP 89116644 A 19890908

Priority

US 25067388 A 19880929

Abstract (en)

[origin: EP0361176A2] In a data processing system of the type including a plurality of processing elements interconnected with each other and with a plurality of memory elements by an interconnection means, a method is provided for accommodating the accessing of a selected memory location in a selected one of the memory elements by at least one requesting processing element to read data stored thereat. The method thereby permits the communication of information between the plurality of processing elements. The method comprising the steps of: sending, from the requesting processing element to the selected memory element via the interconnecting means, a directive including 1) the address of the selected memory location, 2) a comparison value, and 3) an identification of the requesting processing element sending the directive; reading in the selected memory element the data stored at the selected memory location; comparing in the memory element the read data with the comparison value; notifying the requesting processing element via the interconnection means if the read data matches the comparison value; storing the directive in the selected memory element if the read data does not match the comparison value; and repeating the reading, comparing, and notifying steps each time the data in the selected memory location is altered.

IPC 1-7

G06F 9/46

IPC 8 full level

G06F 9/46 (2006.01); **G06F 9/50** (2006.01)

CPC (source: EP US)

G06F 9/52 (2013.01 - EP US); **G06F 9/54** (2013.01 - EP US)

Citation (search report)

- [A] US 3725872 A 19730403 - KEENAN W, et al
- [A] OPERATING SYSTEMS REVIEW (SIGOPS). vol. 21, no. 5, 8 November 1987, NEW YORK US pages 109 - 110; A. CHANG ET AL: '801 Storage: Architecture and Programming'

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Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

EP 0361176 A2 19900404; **EP 0361176 A3 19920826**; **EP 0361176 B1 19950419**; DE 68922261 D1 19950524; DE 68922261 T2 19951102; JP H02236735 A 19900919; US 4965718 A 19901023

DOCDB simple family (application)

EP 89116644 A 19890908; DE 68922261 T 19890908; JP 21213489 A 19890819; US 25067388 A 19880929