

Title (en)
PLASMA DISPLAY PANEL WITH A GREAT ADDRESSABILITY

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Application
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Abstract (en)
[origin: EP0361992A1] The present invention relates to a plasma display panel, the row electrodes (Y1 to Y4) and/or the column electrodes (X1 to X4) of which are arranged to allow in particular a simultaneous, selective-type addressing of several rows (L1 to L4) of pixels (PX1 to PX16). <??>According to the invention, this is obtained, on the one hand, by a scaling down of the row electrodes (Y1 to Y4), and with the aid of an increase in the number of column electrodes (X1 to X4), from which there results a larger number of intersections of column and row electrodes (X1 to X4, Y1 to Y4) than of pixels (PX1 to PX16), and on the other hand, by an increase in the area of the intersections (Sc) intended to form pixels, and by a limitation in the voltages (VY, VX) applied to the electrodes (X1 to X4, Y1 to Y4). <IMAGE>

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