

Title (en)
SPEECH PROCESSING APPARATUS AND METHODS

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Abstract (en)
[origin: WO8810413A1] Speech processing apparatus (1) including a memory (31) for holding prestored information (PHE) indicative of different phonetic representations corresponding to respective sets of addresses (ADR) in the memory (31). Circuitry (CPU1, CPU2 and CPU3) in the apparatus (1) electrically derives a series of coordinate values (Xp, Yp, Zp) of points on a path in a mathematical space from frequency spectra (D(K)) of the speech occurring in successive time intervals respectively, identifies coordinate values (Xp, Yp, Zp) approximating at least one position along the path of a peak (455) in magnitude of acceleration, generates a memory address (ADR) as a function of the position coordinate value (Xp, Yp, Zp) and obtains from the memory (31) the phonetic representation information (PHE) prestored at that memory address (ADR). Methods and other apparatus for speech processing are also disclosed.

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Citation (search report)
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