

Title (en)

Integrated circuit diminishing the inverse current of an inversely polarized transistor.

Title (de)

Integrierbare Schaltungsanordnung zur Rückstromverringerng bei einem invers betriebenen Transistor.

Title (fr)

Circuit intégré diminuant le courant inverse d'un transistor polarisé inversement.

Publication

**EP 0374288 B1 19941012 (DE)**

Application

**EP 88121417 A 19881221**

Priority

EP 88121417 A 19881221

Abstract (en)

[origin: US4945444A] An integratable circuit configuration includes a first transistor of one conduction type having an emitter being acted upon by a first potential, a collector being acted upon by a second potential, a base, a base-to-collector path and a base-to-emitter path. A resistor is connected in parallel with the base-to-emitter path of the first transistor. A second transistor of the other conduction type is connected to the base of the first transistor for triggering. A diode may be connected in parallel with the base-to-collector path of the first transistor. The diode conducts in inverse operation for reverse current reduction during inverse operation of the first transistor. A third transistor may be connected to the first transistor instead of the diode and fourth and further transistors may also be connected to the first transistor.

IPC 1-7

**G05F 1/571**; G05F 1/569

IPC 8 full level

**G05F 1/569** (2006.01); **G05F 1/571** (2006.01)

CPC (source: EP US)

**G05F 1/569** (2013.01 - EP US); **G05F 1/571** (2013.01 - EP US)

Cited by

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Designated contracting state (EPC)

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**EP 0374288 A1 19900627**; **EP 0374288 B1 19941012**; AT E112868 T1 19941015; DE 3851839 D1 19941117; US 4945444 A 19900731

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