

Title (en)

METHOD TO SUPPRESS AMPLITUDE VARIATIONS OF TWO ALTERNATING, PERIODIC SIGNALS IN PHASE QUADRATURE WITH A RANDOM PHASE SEQUENCE, AND CIRCUIT ARRANGEMENT TO CARRY OUT THE METHOD

Publication

EP 0374641 A3 19910918 (DE)

Application

EP 89122722 A 19891208

Priority

DE 3843108 A 19881221

Abstract (en)

[origin: EP0374641A2] The invention relates to a method of suppressing amplitude variations of two electrical signals (uS_1, uS_2) in phase quadrature and a circuit arrangement for carrying out the method. According to the invention, the positive or negative amplitude variation of the signals (uS_1, uS_2) is monitored for undershooting or overshooting of an upper reference voltage (+/-US- or +/-US+) in such a way that in the event of undershooting or overshooting a device (20, 22) for changing a prescribed manipulated variable of an actuator (8, 10) is activated, and the signals (uS_1, uS_2) are respectively converted into rectangular signals (uRS_1, uRS_2), from which clock pulses (uC_{11} or uC_{12}) are generated by means of a logic circuit (24 or 26) depending upon the phase sequence of the signals (uS_1, uS_2) at the positive or negative flanks of the rectangular signals (uRS_1, uRS_2), which vary the prescribed gain as a function of the result of the amplitude monitoring by a prescribed value (LSB). This yields a method of suppressing amplitude variations of two electrical alternating, periodic signals (uS_1, uS_2) in phase quadrature with a random phase sequence, and a circuit arrangement for carrying out the method that operates independently of frequency. <IMAGE>

IPC 1-7

G05F 1/45

IPC 8 full level

G05F 1/44 (2006.01); **G05F 1/45** (2006.01)

CPC (source: EP)

G05F 1/44 (2013.01); **G05F 1/452** (2013.01)

Citation (search report)

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Designated contracting state (EPC)

AT CH DE FR GB IT LI

DOCDB simple family (publication)

EP 0374641 A2 19900627; EP 0374641 A3 19910918; EP 0374641 B1 19950215; AT E118626 T1 19950315; DE 3843108 C1 19900215; DE 58909003 D1 19950323

DOCDB simple family (application)

EP 89122722 A 19891208; AT 89122722 T 19891208; DE 3843108 A 19881221; DE 58909003 T 19891208