

Title (en)

Method and apparatus for handling multiple condition codes as for a parallel pipeline computer

Title (de)

Verfahren und Vorrichtung zur Verarbeitung von mehreren Zustandscodes wie für einen Parallel-Pipeline-Rechner

Title (fr)

Méthode et dispositif de traitement de plusieurs codes de condition tel que pour un calculateur pipeline parallèle

Publication

**EP 0378830 B1 19961023 (EN)**

Application

**EP 89123426 A 19891219**

Priority

US 29485089 A 19890109

Abstract (en)

[origin: EP0378830A2] A computer system includes a condition register having multiple fields. Each field may be used as an independent condition register. A compiler which generates executable code for the computer system assigns instructions to different fields, allowing condition values to be saved while other conditions are evaluated and allowing consecutive instructions which generate or test condition values to be overlapped in their execution.

IPC 1-7

**G06F 9/32**; **G06F 9/38**

IPC 8 full level

**G06F 9/32** (2006.01); **G06F 9/38** (2006.01)

CPC (source: EP US)

**G06F 9/30094** (2013.01 - EP US); **G06F 9/3885** (2013.01 - EP US)

Cited by

EP0518420A3; FR2693573A1; US5983335A; US5881308A; US5630157A

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**EP 0378830 A2 19900725**; **EP 0378830 A3 19920902**; **EP 0378830 B1 19961023**; DE 68927371 D1 19961128; DE 68927371 T2 19970410; JP H02226342 A 19900907; JP H0776921 B2 19950816; US 5125092 A 19920623

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