

Title (en)  
Delay compensated jack.

Title (de)  
Stecker mit kompensierter Verzögerung.

Title (fr)  
Fiche à temporisation compensée.

Publication  
**EP 0380210 A2 19900801 (EN)**

Application  
**EP 90300227 A 19900109**

Priority  
US 30371989 A 19890127

Abstract (en)  
A delay compensated jack (40) is disclosed having a jack body (42) with first (52), second (53) and third ports (46). A first signal pathway (E) is provided for electrically connecting a first cable and a second cable where said first and second cables are connected to said first and second ports, respectively. A second signal pathway (F) is provided for electrically connecting a first cable and a third cable when said first and third cables are connected to said first and third ports, respectively. Said first signal pathway includes a circuit member (68) selected to induce a pre-determined signal compensation into a signal passing through the first pathway. A switch mechanism (156) is provided for opening the first pathway when the third cable is connected to the third port.

IPC 1-7  
**H01P 1/12**; **H01R 13/70**; **H01R 13/703**; **H03H 1/00**

IPC 8 full level  
**H01R 13/646** (2006.01); **H01R 13/70** (2006.01); **H01R 24/02** (2006.01)

CPC (source: EP)  
**H01R 24/46** (2013.01); **H01R 24/542** (2013.01); **H01R 2103/00** (2013.01)

Cited by  
US6139369A; US5467062A; AU676994B2; US6632106B2; US6554652B1; WO9907040A1; WO9320600A1

Designated contracting state (EPC)  
AT BE CH DE DK ES FR GB GR IT LI LU NL SE

DOCDB simple family (publication)  
**EP 0380210 A2 19900801**; **EP 0380210 A3 19901219**; CA 2005344 A1 19900727; CN 1044550 A 19900808; JP H02260380 A 19901023

DOCDB simple family (application)  
**EP 90300227 A 19900109**; CA 2005344 A 19891213; CN 90100432 A 19900125; JP 1766790 A 19900126