

Title (en)
METHOD AND APPARATUS FOR THE DEMODULATION OF BINARY PSK SIGNALS

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EP 0390351 A3 19920401 (EN)

Application
EP 90302381 A 19900306

Priority
US 32029589 A 19890307

Abstract (en)
[origin: CA2011592A1] A multiple phase PSK demodulator for use in a spread spectrum communication system includes a plurality of baseband frequency converters coupled to a corresponding plurality of differential decoders in order to directly recover the encoded PSK data. The logic outputs of the plurality of differential data decoders are coupled to a majority vote logic circuit for determining the correct logic level output signal of the plurality of differential data decoders. In another embodiment, a three phase resistive network is used to convert the two phase quadrature output of two baseband frequency converters to plural phase outputs coupled to the inputs of a corresponding plurality of differential data decoders.

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CPC (source: EP US)
H04L 27/2332 (2013.01 - EP US)

Citation (search report)
• [X] US 4715047 A 19871222 - HAMBLEY ALLAN R [US]
• [Y] US 4416016 A 19831115 - IAPICCO JEFFREY A [US]
• [X] PATENT ABSTRACTS OF JAPAN, vol. 9, no. 136 (E-320)[1859], 12th June 1985; & JP-A-60 18 046 (CLARION K.K.) 30-01-1985

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US5517530A; EP0724802A4; WO9513677A1

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US 4942591 A 19900717; CA 2011592 A1 19900907; EP 0390351 A2 19901003; EP 0390351 A3 19920401; JP H02299344 A 19901211

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