Title (en)
PARALLEL STRING PROCESSOR AND METHOD FOR A MINICOMPUTER

Publication
EP 0395636 A4 19911016 (EN)

Application
EP 88906247 A 19880407

Priority
US 8842187 A 19870820

Abstract (en)
[origin: WO8901653A1] A processor for use in a computer system for comparing a number of bytes simultaneously in order to locate a control character in a string of data. The processor includes a register for holding the data bytes (REGB), a register for storing the control characters (REGA), a comparison circuit (CMP) for simultaneously comparing the bytes of data stored in the two registers, and a circuit for generating indicator bits when a match has been found (100). In another aspect, a parallel string processor includes a first register (136) which stores a keyword string and a pair of interconnected shift registers (186 and 188), which stores the string to be searched for the presence of the keyword. An arithmetic logic unit (140) compares the shift registers to determine whether the keyword is present in the portion of the string being searched. After each comparison, the contents of the interconnected shift registers are shifted with respect to the keyword stored in the first register. When the processor is searching for the presence of a keyword having a predetermined number of bytes, the contents of the shift register are shifted a byte at a time, and when the processor is searching for the presence of a keyword having a predetermined number of bits the contents of the shift register are shifted one bit at a time.

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G05B 1/00; G06F 7/02

IPC 8 full level
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CPC (source: EP KR)
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Citation (search report)
- [Y] US 4560974 A 19851224 - COLEMAN GUY B [US], et al
- [Y] GB 2036390 A 19800625 - STANDARD TELEPHONES CABLES LTD
- See references of WO 8901653A1

Designated contracting state (EPC)
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