

Title (en)

ELECTRICAL LOAD SHEDDING CIRCUIT

Publication

EP 0402362 A4 19911113 (EN)

Application

EP 89901251 A 19890106

Priority

AU PI689988 A 19880223

Abstract (en)

[origin: WO8908342A1] An apparatus for controlling the load of an alternating-current electricity supply system supplying power to a plurality of consumers. At each consumer's premises the load (1) is connected to an AC mains (2) via a controllable load switch (3). Across the mains is coupled a frequency measuring means (4, 5 and 6) whose output is compared in comparators (7, 8) with predetermined threshold values stored in limit registers (9, 10). The threshold values correspond to frequencies below the normal frequency of the power supplied. When a threshold value is exceeded due to a fall in frequency, an interrupt signal is provided at the output of one or other comparator, depending on the severity of the fall in frequency. The interrupt signal is extended to an input of a processor (11) which operates the load switch after a predetermined delay provided by delay registers (13, 14) associated with the comparators, and disconnects the load from the mains.

IPC 1-7

H02H 3/46; H02H 3/06

IPC 8 full level

H02H 3/46 (2006.01); **H02J 3/14** (2006.01)

CPC (source: EP KR)

H02H 3/46 (2013.01 - EP KR); **H02J 3/14** (2013.01 - EP); **H02J 2310/12** (2020.01 - EP); **Y02B 70/3225** (2013.01 - EP);
Y04S 20/222 (2013.01 - EP)

Citation (search report)

- [A] US 4231029 A 19801028 - JOHNSTON PAUL M
- See references of WO 8908342A1

Designated contracting state (EPC)

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DOCDB simple family (publication)

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JP H03503833 A 19910822; KR 900701072 A 19900817

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