

Title (en)
THERMAL HEAD PRINTER

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EP 0413413 A3 19910814 (EN)

Application
EP 90306180 A 19900607

Priority
JP 21271689 A 19890818

Abstract (en)
[origin: EP0413413A2] A thermal print head (20) includes a plurality, e.g., three delay shift registers (31,32,33) operating by a clock-controlled shift scan to receive dot print signals (D) from a dot print signal generating circuit (10). The delay registers (31,32,33) have a number of addresses corresponding to the number of dot print elements (21) in the printer head (20). The dot print signals (D) from the delay shift registers (31,32,33) are applied to an OR gate (34) connected to shift registers (24) for the dot print elements (21). With this construction, the shift scan cycle it is set so that $t \leq T/(n + 1)$, where T is the least print cycle under normal printing conditions and n is the number of delay shift registers (31,32,33). The real time signals are obtained by sampling an analogue wave as a digitized value and counting the number of clock pulse needed to equal this digitized value, at which time a signal is given.

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CPC (source: EP)
B41J 2/355 (2013.01)

Citation (search report)
• [A] EP 0304916 A1 19890301 - NEC CORP [JP], et al
• [A] US 4630068 A 19861216 - IMS J ROBERT [US]
• [A] PATENT ABSTRACTS OF JAPAN vol. 11, no. 172 (M-595)(2619), 3 June 1987; & JP - A - 62003970 (KYOCERA CORP.) 09.01.1987
• [A] PATENT ABSTRACTS OF JAPAN vol. 12, no. 116 (M-684)(2963), 13 April 1988; & JP - A - 62244664 (MATSUSHITA ELECTRIC IND CO LTD) 26.10.1987
• [A] PATENT ABSTRACTS OF JAPAN vol. 10, no. 360 (M-541)(2417), 3 December 1986; & JP - A - 61158476 (MATSUSHITA ELECTRIC IND CO LTD) 18.07.1986

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