

Title (en)

METHOD FOR INTERMEDIATE AMPLIFICATION OF DIGITAL SIGNALS AND INTERMEDIATE REPEATER FOR DIGITAL SIGNALS

Publication

EP 0416291 B1 19930421 (DE)

Application

EP 90114889 A 19900802

Priority

DE 3927681 A 19890822

Abstract (en)

[origin: EP0416291A2] The method for intermediate amplification of digital signals with clock regeneration determines the phase difference between the reception timing signal and the local transmission timing signal and modifies the phase of the local transmission timing signal depending on the phase difference. It is particularly advantageous if the reception timing signal is used as the transmission timing signal as soon as the phase difference is essentially zero. It is possible as a result to minimise the start-up delay and also the packet spacing in that the number of bits which are to be temporarily stored can be kept down. Intermediate repeaters to carry out the method are indicated. <IMAGE>

IPC 1-7

H04B 3/04; **H04B 3/58**; **H04L 12/02**; **H04L 25/03**

IPC 8 full level

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CPC (source: EP US)

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EP 0416291 A2 19910313; **EP 0416291 A3 19920226**; **EP 0416291 B1 19930421**; AT E88601 T1 19930515; DE 3927681 A1 19910314; DE 3927681 C2 19930527; DE 59001242 D1 19930527; ES 2040011 T3 19931001; US 5210773 A 19930511

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