

Title (en)

PLL demodulating circuit in traffic information receiver.

Title (de)

PLL-Demodulationsschaltung in einem Verkehrsnachrichtenempfänger.

Title (fr)

Circuit PLL démodulateur dans un récepteur d'informations de trafic.

Publication

EP 0420584 A2 19910403 (EN)

Application

EP 90310493 A 19900925

Priority

JP 25113589 A 19890927

Abstract (en)

In a traffic information receiver, the total of the locking ranges of a PLL circuit (17 for demodulating an SDK signal and a PLL circuit (10) for demodulating a DK signal is so determined as not to exceed the difference between the maximum frequency of the BK signal and the frequency of the DK signal. Even under an influence by a multi-path phenomenon, such a situation can be avoided that the locking frequency of the PLL circuit (10) for DK coincides with the frequency of the BK signal to cause erroneous detection of the BK signal.

IPC 1-7

G08G 1/09

IPC 8 full level

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