Title (en)

Circuit for driving a liquid crystal panel.

Title (de)

Treiberschaltung für eine Flüssigkristallanzeigetafel.

Title (fr)

Circuit de commande d'un panneau d'affichage à cristaux liquides.

Publication

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Application

EP 90120478 A 19901025

Priority

JP 27718989 A 19891026

Abstract (en)

According to the present invention, there is provided a circuit for driving a liquid crystal panel includes a plurality of shift registers (11Aa, 12Aa, ..., 1mAa) connected in cascade on an A array for transferring data in one direction, a plurality of shift registers (11Bc, 12Bc, ..., 1mBc) on a B array for transferring the data transferred from the shift registers (11Aa, 12Aa, ..., 1mAa) in a direction opposite to the one direction, and a plurality of circuit blocks (11Ac, 12Ac, ..., 1mAc; 11Bc, 12Bc, ..., 1mBc) on the A and B arrays for temporarily storing the data transferred from the shift registers (11Aa, 12Aa, ..., 1mAa; 11Bc, 12Bc, ..., 1mBc). The circuit blocks (11Ac, 12Ac, ..., 1mAc; 11Bc, 12Bc, ..., 1mBc) include output stages for supplying the data outside a semiconductor chip, and the output stages are constituted of MOS transistors (3A3, 3B3). The MOS transistors of the first circuit block (11Ac) on the A array and the m-th circuit block (1mBc) of the B array, those of the second circuit block (12Ac) on the A array and the (m-1)-th circuit block (1m-1Bc) on the B array, ..., those of the m-th circuit block (1mAc) on the A array and the first circuit block (11Bc) on the B array are symmetrically arranged, respectively. The currents flow through the symmetrical MOS transistors in the same direction.

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G09G 3/36

IPC 8 full level

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