Title (en)
COLOR TRANSLATION CIRCUIT

Publication
EP 0431754 A3 19910814 (EN)

Application
EP 90312089 A 19901105

Priority
US 44730689 A 19891207

Abstract (en)
[origin: EP0431754A2] Disclosed is a color translation circuit utilizing a monitor circuit to identify and prioritize windows to be displayed in a multi window environment on a screen. Through the use of the monitor circuitry the size of video memory is reduced.

IPC 1-7
G09G 5/06; G09G 5/14

IPC 8 full level
H04N 9/74 (2006.01); G06T 11/60 (2006.01); G09G 5/06 (2006.01); G09G 5/14 (2006.01)

CPC (source: EP)
G09G 5/06 (2013.01); G09G 5/14 (2013.01)

Citation (search report)
- [XP] EP 0392551 A2 19901017 - BROOKTREE CORP [US]
- [A] EP 0329892 A2 19890830 - IBM [US]
- [A] US 4550315 A 19851029 - BASS LELAND J [US], et al
- [A] EP 0168144 A2 19860115 - NORTHERN TELECOM LTD [CA]
- [X] COMPUTER DESIGN. vol. 28, no. 19, October 1, 1989, LITTLETON, MASSACHUSETTS US page 105; TOM WILLIAMS: 'RAMDAC SOLVES WINDOW PALETTE CONFLICTS '

Cited by
EP0482746A3; EP0497344A3; US5388192A

Designated contracting state (EPC)
AT BE CH DE DK ES FR GB GR IT LI LU NL SE

DOCDB simple family (publication)
EP 0431754 A2 19910612; EP 0431754 A3 19910814; JP H03182796 A 19910808

DOCDB simple family (application)
EP 90312089 A 19901105; JP 34115990 A 19901129