

Title (en)

Balanced drive symmetric MIM diode configuration for liquid crystal displays and method of operating same.

Title (de)

Diodenmuster mit balancierter symmetrischer Ansteuerung für Flüssigkristallanzeigen und Steuerverfahren dafür.

Title (fr)

Configuration de diodes MIM à commande balancée symétrique pour affichages à cristaux liquides et sa méthode de commande.

Publication

EP 0434627 A2 19910626 (EN)

Application

EP 90810989 A 19901214

Priority

US 45182789 A 19891218

Abstract (en)

An active matrix liquid crystal display (10) and method for rapidly storing and efficiently retaining electric charge in selected ones of a plurality of storage elements (46) are disclosed. Each storage element (46) is addressable through a pair of parallel row select address lines (12, 12 min , 14, 14 min) and a column address line (18, 20). A pair of MIM devices (34, 36) are coupled between the row select address lines (12, 12 min) and the storage element (46) is coupled between the common node (38) of the MIM devices (34, 36) and the column address line (18) to which charging potential is applied. During the charging of the storage element, operating potentials which are substantially equal and opposite in polarity are applied to the row select line pair to forward bias both MIM devices and thereafter operating potentials which are also substantially equal and opposite in polarity are applied to the two select line pair to reverse bias both MIM devices to provide retention of the charge in the storage element. <IMAGE>

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G09G 3/36

IPC 8 full level

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CPC (source: EP)

G09G 3/367 (2013.01); **G09G 2300/0823** (2013.01); **G09G 2300/0895** (2013.01)

Cited by

EP0910062A3; EP0951008A3; EP0475770A3; US55576728A; US6008872A; US5926236A; EP1898256A3; US6243062B1; US11183585B2; US6738035B1; US11610809B2; US6222596B1; WO9526544A1; WO0028516A1; US6225968B1; US11069799B2; EP0942315B1; WO9600479A3; WO9905644A3

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