

Title (en)

METHOD AND APPARATUS FOR DETECTING A FRAME ALIGNMENT WORD IN A DATA STREAM

Publication

**EP 0443754 A3 19920401 (EN)**

Application

**EP 91301061 A 19910211**

Priority

GB 9004188 A 19900223

Abstract (en)

[origin: EP0443754A2] A method and apparatus is provided for detecting a frame alignment word in a data stream. The apparatus comprises a storage means (9) arranged to receive a data stream and pass each bit of the data stream serially through each location of the storage means. A template pattern means (10), arranged to generate a template pattern, is connected to the storage means (9) and arranged in bit groups, each group generating an output signal when it identifies a group of bits corresponding to its template pattern. A decoder circuit (B) is arranged to receive the output signals from the groups and generate a decoder output signal when a specified number of groups match the template pattern indicating that the frame alignment word has been detected. <IMAGE>

IPC 1-7

**H04J 3/06**

IPC 8 full level

**H04J 3/06** (2006.01); **H04J 3/14** (2006.01)

CPC (source: EP US)

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Citation (search report)

- [X] GB 1069538 A 19670517 - TELEFUNKEN PATENT
- [A] US 4835768 A 19890530 - HUBBARD WILLIAM M [US], et al
- [X] I.B.M. TECHNICAL DISCLOSURE BULLETIN vol. 23, no. 3, 1 August 1988, NEW YORK (US) pages 1220 - 1221; P.HODGES: 'Special bit pattern detector employing majority logic'
- [A] \* page 1220, line 7 - page 1221, line 2 \*
- [Y] PATENT ABSTRACTS OF JAPAN vol. 011, no. 240 (E-529) & JP-A-62 051 849 ( MITSUBISHI ELECTRIC CORP. ) 6 March 1987

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