

Title (en)
METHOD AND APPARATUS FOR DETECTING A FRAME ALIGNMENT WORD IN A DATA STREAM

Publication
EP 0443754 A3 19920401 (EN)

Application
EP 91301061 A 19910211

Priority
GB 9004188 A 19900223

Abstract (en)
[origin: EP0443754A2] A method and apparatus is provided for detecting a frame alignment word in a data stream. The apparatus comprises a storage means (9) arranged to receive a data stream and pass each bit of the data stream serially through each location of the storage means. A template pattern means (10), arranged to generate a template pattern, is connected to the storage means (9) and arranged in bit groups, each group generating an output signal when it identifies a group of bits corresponding to its template pattern. A decoder circuit (B) is arranged to receive the output signals from the groups and generate a decoder output signal when a specified number of groups match the template pattern indicating that the frame alignment word has been detected. <IMAGE>

IPC 1-7
H04J 3/06

IPC 8 full level
H04J 3/06 (2006.01); **H04J 3/14** (2006.01)

CPC (source: EP US)
H04J 3/0608 (2013.01 - EP US)

Citation (search report)
• [X] GB 1069538 A 19670517 - TELEFUNKEN PATENT
• [A] US 4835768 A 19890530 - HUBBARD WILLIAM M [US], et al
• [X] I.B.M. TECHNICAL DISCLOSURE BULLETIN vol. 23, no. 3, 1 August 1988, NEW YORK (US) pages 1220 - 1221; P.HODGES: 'Special bit pattern detector employing majority logic'
• [A] * page 1220, line 7 - page 1221, line 2 *
• [Y] PATENT ABSTRACTS OF JAPAN vol. 011, no. 240 (E-529) & JP-A-62 051 849 (MITSUBISHI ELECTRIC CORP.) 6 March 1987

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Designated contracting state (EPC)
AT BE CH DE DK ES FR GR IT LI LU NL SE

DOCDB simple family (publication)
EP 0443754 A2 19910828; EP 0443754 A3 19920401; AU 635112 B2 19930311; AU 7124091 A 19910829; CA 2036545 A1 19910824; CN 1025267 C 19940629; CN 1054344 A 19910904; FI 910865 A0 19910222; FI 910865 A 19910824; GB 2241413 A 19910828; GB 2241413 B 19941005; GB 9004188 D0 19900418; GB 9102844 D0 19910327; JP H04216230 A 19920806; PT 96866 A 19930129; US 5204859 A 19930420

DOCDB simple family (application)
EP 91301061 A 19910211; AU 7124091 A 19910220; CA 2036545 A 19910218; CN 91101196 A 19910223; FI 910865 A 19910222; GB 9004188 A 19900223; GB 9102844 A 19910211; JP 4905891 A 19910221; PT 9686691 A 19910225; US 65494991 A 19910213