

Title (en)

Drive circuit for dot matrix display.

Title (de)

Steuereinrichtung für eine Bildpunktmatrixanzeige.

Title (fr)

Circuit de commande d'un affichage à matrice de points.

Publication

EP 0447919 A2 19910925 (EN)

Application

EP 91103736 A 19910312

Priority

JP 6193490 A 19900313

Abstract (en)

A drive circuit for a dot matrix liquid crystal display which receives polarity changing drive voltages in different fields, comprising segment drivers for supplying low voltage picture signals and common drivers for supplying a first voltage having a large voltage difference from the picture signal during a selected time and a second voltage having a small voltage difference from the picture signal during non-selected time.

IPC 1-7

G09G 3/36

IPC 8 full level

G02F 1/133 (2006.01); **G09G 3/20** (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)

G09G 3/3614 (2013.01); **G09G 3/3681** (2013.01); **G09G 3/3692** (2013.01); **G09G 3/3696** (2013.01); **G09G 2330/02** (2013.01)

Cited by

CN100345034C; EP0726558A1; EP0721137A4; US5859632A; CN100373440C; WO2004003883A1

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