

Title (en)

A data processing system provided with a performance enhancing instruction cache

Title (de)

Datenverarbeitungssystem mit einem leistungsverbessernden Befehlscachespeicher

Title (fr)

Dispositif de traitement de données avec une antémémoire d'instruction améliorant l'efficacité

Publication

EP 0449369 B1 19980729 (EN)

Application

EP 91200656 A 19910325

Priority

US 50061290 A 19900327

Abstract (en)

[origin: US5701435A] A system for increasing the speed and efficiency of instruction execution by a computer processing system. An instruction cache is provided to receive a minor number of stored instructions for execution by the computer processing system. The instructions are prefetched and returned in cache based upon an analysis of instructions which are in the cache pending execution. Target instructions of branch instructions may be prefetched as a result of the analysis of a branch instruction pending in the cache. Other instructions may be retained in cache when they are tagged as being likely to be reused.

IPC 1-7

G06F 9/38

IPC 8 full level

G06F 9/38 (2006.01); **G06F 12/08** (2006.01)

CPC (source: EP US)

G06F 9/3808 (2013.01 - EP US); **G06F 9/381** (2013.01 - EP US); **G06F 12/0862** (2013.01 - EP US); **G06F 12/0875** (2013.01 - EP US); **G06F 2212/6028** (2013.01 - EP US)

Cited by

WO02061574A1; GB2247546B; EP0912006A3; US5774164A; EP0488567A3; US7873813B2

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DOCDB simple family (publication)

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DOCDB simple family (application)

US 6384593 A 19930519; DE 69129872 T 19910325; EP 91200656 A 19910325; JP 8584291 A 19910327