

Title (en)

Procedure for regulating the D.C. voltage of a rectifier.

Title (de)

Verfahren zur Regelung der Gleichspannung eines Gleichrichters.

Title (fr)

Procédé pour la régulation de la tension continue d'un redresseur.

Publication

EP 0455181 B1 19950705 (EN)

Application

EP 91106880 A 19910427

Priority

FI 902226 A 19900503

Abstract (en)

[origin: EP0455181A2] Procedure for regulating the d.c. voltage of a pulse- width modulated rectifier bridge controlled by self-commutated semiconductor components (T1-T6) and provided with filtering coils (L), in which procedure the phase voltages (Er,Es,Et) of the supplying three-phase mains (L1,L2,L3) and the d.c. voltage (Udc) of the rectifier are measured. The modulation references (Umr,Ums,Umt) needed for the pulse-width modulation are produced using pure sine and cosine curves synchronized with the mains voltages, with localized vectors formed from them or with components of such vectors by means of a phase-locked loop (PLL2). The regulation is effected in a coordinate system tied to the localized vector (E) of the mains voltages. <IMAGE>

IPC 1-7

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IPC 8 full level

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CPC (source: EP US)

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Cited by

AT4111414B; EP0711028A3; US5717583A; EP2302783A1; CN102035215A; US6850426B2; US7308614B2; US8014181B2; WO03094334A3

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