

Title (en)

SYSTEM FOR DEBUGGING SHARED MEMORY MULTIPROCESSOR COMPUTERS

Publication

EP 0455946 A3 19921028 (EN)

Application

EP 91102866 A 19910227

Priority

US 51971990 A 19900507

Abstract (en)

[origin: EP0455946A2] System for debugging application programs being executed in a parallel processor computer system. A debugging program running in one processor identifies data events occurring on a shared memory bus as significant breakpoints to identify data errors. A bus monitor is programmed to detect the data events as they occur in the shared memory bus. A trace memory is provided to record the detected data events along with a time stamp. The trace memory may be read and the program analyzed for the determination of data errors which may have occurred. <IMAGE>

IPC 1-7

G06F 11/00

IPC 8 full level

G06F 11/28 (2006.01); **G06F 11/36** (2006.01); **G06F 15/16** (2006.01); **G06F 15/177** (2006.01)

CPC (source: EP)

G06F 11/364 (2013.01)

Citation (search report)

- [Y] EP 0189848 A2 19860806 - HEWLETT PACKARD CO [US]
- [A] US 3540003 A 19701110 - MURPHY ROBERT W
- [A] FR 2454138 A1 19801107 - MATERIEL TELEPHONIQUE
- [A] DE 2835764 A1 19800228 - SIEMENS AG
- [Y] EP 0130467 A2 19850109 - IBM [US]

Cited by

US7840787B2; US6678838B1; EP1039386A1; EP0897151A1; US6026503A; EP1475713A1; CN1328635C; CN108196945A; EP0683455A1; FR2720173A1; US5819023A; US6993453B2; WO02086727A1; WO2010052678A1; US7500141B2

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

EP 0455946 A2 19911113; EP 0455946 A3 19921028; JP H04229340 A 19920818

DOCDB simple family (application)

EP 91102866 A 19910227; JP 9814391 A 19910403