

Title (en)

SYSTEM FOR DEBUGGING SHARED MEMORY MULTIPROCESSOR COMPUTERS

Publication

**EP 0455946 A3 19921028 (EN)**

Application

**EP 91102866 A 19910227**

Priority

US 51971990 A 19900507

Abstract (en)

[origin: EP0455946A2] System for debugging application programs being executed in a parallel processor computer system. A debugging program running in one processor identifies data events occurring on a shared memory bus as significant breakpoints to identify data errors. A bus monitor is programmed to detect the data events as they occur in the shared memory bus. A trace memory is provided to record the detected data events along with a time stamp. The trace memory may be read and the program analyzed for the determination of data errors which may have occurred. <IMAGE>

IPC 1-7

**G06F 11/00**

IPC 8 full level

**G06F 11/28** (2006.01); **G06F 11/36** (2006.01); **G06F 15/16** (2006.01); **G06F 15/177** (2006.01)

CPC (source: EP)

**G06F 11/364** (2013.01)

Citation (search report)

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DOCDB simple family (application)

**EP 91102866 A 19910227**; JP 9814391 A 19910403