Title (en)

Input matching circuit and method for adjusting the same.

Title (de)

Eingangsanpassungsnetzwerk und Verfahren zur Einstellung desselben.

Title (fr)

Circuit d'entrée d'adaptation et méthode pour l'ajuster.

Publication

EP 0456207 B1 19951025 (EN)

Application

EP 91107478 A 19910508

Priority

JP 11953390 A 19900509

Abstract (en)

[origin: EP0456207A2] An input matching network in an input circuit of an amplifier comprises a basic input matching circuit (50) including a serial inductance, a strip line of an approximately quarter wavelength connected in series to the basic matching circuit, and a parallel capacitance including an open stub (S1) connected between the strip line and the basic input matching circuit (50). An electrical length (l epsilon) of the open stub (S1) is selected such that a phase angle of a signal source reflection coefficient as viewed from the amplifier is larger than a mean value of manufacturing variations of a phase angle of an optimum signal source power reflection coefficient of the amplifier. By shortening the electrical length (l epsilon) of the open stub (S1) by cutting it by a laser, a matching point can be adjusted to comply to substantially entire distribution of the manufacturing variations of the optimum signal source power reflection coefficient of the amplifier. <IMAGE>

IPC 1-7

H01P 5/02

IPC 8 full level

H01P 5/02 (2006.01); **H03F 3/60** (2006.01)

CPC (source: EP)

H01P 5/02 (2013.01)

Citation (examination)

Avasarala et.al. "A 2.5 Watt high Efficiency X-Band Power MMIC", Digest of Papers of the IEEE 1989 Microwave and millimeter-wave Monolithic Circuits Symposium, pages 25-28.

Designated contracting state (EPC)

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DOCDB simple family (publication)

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DOCDB simple family (application)

EP 91107478 A 19910508; CA 2041729 A 19910502; DE 69114046 T 19910508; JP 11953390 A 19900509