

Title (en)

Semiconductor device and method of screening the same.

Title (de)

Halbleiteranordnung und ihre Prüfungsverfahren.

Title (fr)

Dispositif à semi-conducteurs et méthode pour l'examiner.

Publication

EP 0456254 A2 19911113 (EN)

Application

EP 91107613 A 19910510

Priority

JP 11994890 A 19900511

Abstract (en)

A semiconductor device comprising a plurality of circuit blocks (12, 13, 102, 103) to which various potentials, including at least one potential either raised or lowered, are assigned. The device further comprises means (11, 101) for selectively and reversely changing the potentials assigned to the circuit blocks. <IMAGE> <IMAGE>

IPC 1-7

G11C 5/14; G11C 29/00

IPC 8 full level

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CPC (source: EP KR US)

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JP 2533221 B2 19960911; JP H0417191 A 19920121; KR 910020731 A 19911220; KR 950014679 B1 19951213; US 5428576 A 19950627;
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