Title (en)

Interrupt processing allocation in a multiprocessor system

Title (de)

Unterbrechungsverarbeitungszuordnung in einem Mehrprozessorsystem

Title (fr)

Allocation de traitement d'interruption dans un système multiprocesseur

Publication

EP 0459714 B1 19980527 (EN)

Application

EP 91304719 A 19910524

Priority

US 53319290 A 19900601

Abstract (en)

[origin: EP0459714A2] This invention relates to an arrangement for distributing interrupts within a multiprocessing system and for processing personal computer (PC) interrupts as well as multiprocessor interrupts in that system. New interrupts are diverted from processors that have recently processed an interrupt, thus preventing any one processor from being overloaded with an interrupt processing work load. Some of the processors are equipped to process PC interrupts and multiprocessing system interrupts. In each processor classes of interrupts may be masked out. A distributed arbitrator is used to allocate an interrupt among a plurality of processors available to process the interrupt.

IPC 1-7

G06F 9/46; G06F 13/26

IPC 8 full level

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CPC (source: EP US)

G06F 9/4812 (2013.01 - EP US); **G06F 9/505** (2013.01 - EP US); **G06F 13/26** (2013.01 - EP US)

Cited by

EP0511476A3; US6535942B1; US8386684B2; US6701429B1; WO2009074946A1; WO0159567A3; WO0033175A3

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