

Title (en)

METHODS AND APPARATUS FOR EFFICIENT RESOURCE ALLOCATION FOR ERROR AND EXCEPTION HANDLING IN CONVERGENT DIVISION

Publication

**EP 0461240 A4 19930811 (EN)**

Application

**EP 91902487 A 19901217**

Priority

US 45909089 A 19891229

Abstract (en)

[origin: WO9110190A1] A method which utilizes a single preprocessing step (214, 216) to modify a divisor (D) in floating point division and to enable an exception correction and error checking operation (104, 108) to run concurrently with a division operation. The invention permits achievement of the correct predetermined result if an exception is present or the quotient if no exception occurs.

IPC 1-7

**G06F 7/52**

IPC 8 full level

**G06F 7/483** (2006.01); **G06F 7/499** (2006.01); **G06F 7/52** (2006.01); **G06F 7/535** (2006.01)

CPC (source: EP KR)

**G06F 7/52** (2013.01 - KR); **G06F 7/535** (2013.01 - EP); **G06F 7/4873** (2013.01 - EP); **G06F 7/49905** (2013.01 - EP); **G06F 7/49926** (2013.01 - EP); **G06F 2207/5355** (2013.01 - EP)

Citation (search report)

- [AP] EP 0377992 A2 19900718 - IBM [US]
- [A] WEITEK APPLICATION NOTE 'WTL 1032/1033 Floating Point Division/Square Root/ IEEE Arithmetic' 1983 , WEITEK CORPORATION , SANTA CLARA, CA, USA
- See references of WO 9110190A1

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

**WO 9110190 A1 19910711**; CA 2047180 A1 19910630; EP 0461240 A1 19911218; EP 0461240 A4 19930811; JP H04504774 A 19920820; KR 920701904 A 19920812; KR 940008617 B1 19940924

DOCDB simple family (application)

**US 9007431 W 19901217**; CA 2047180 A 19901217; EP 91902487 A 19901217; JP 50282691 A 19901217; KR 910701004 A 19910827