

Title (en)

DSP BASED RADIO WITH DIMINISHED POWER REQUIREMENTS

Publication

EP 0463104 A4 19930113 (EN)

Application

EP 90905882 A 19900313

Priority

US 32560389 A 19890320

Abstract (en)

[origin: WO9011652A1] A radio (100) having two signal processing paths, one including a digital signal processor (104) that consumes a relatively large amount of power and one including a signal processor (106) that consumes a relatively small amount of power. The low power processor (106) operates to monitor for broadcast signals of interest. Upon detecting such a signal, the low power processor (106) enables the digital signal processor (104) to facilitate proper signal processing.

IPC 1-7

H04B 1/16

IPC 8 full level

H04B 1/40 (2006.01); **H04B 1/16** (2006.01); **H04W 52/02** (2009.01)

CPC (source: EP KR)

H04B 1/16 (2013.01 - KR); **H04W 52/0283** (2013.01 - EP); **H04W 52/0293** (2013.01 - EP); **Y02D 30/70** (2020.08 - EP)

Citation (search report)

- [Y] US 3753122 A 19730814 - SHACKLEFORD K
- [A] WO 8806830 A1 19880907 - RODRIGUES MICHAEL VICTOR [GB]
- [X] PATENT ABSTRACTS OF JAPAN vol. 11, no. 88 (E-2535)18 March 1987 & JP-A-61 240 719 (NEC CORP)
- See references of WO 9011652A1

Designated contracting state (EPC)

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DOCDB simple family (publication)

WO 9011652 A1 19901004; CA 2045519 A1 19900921; CA 2045519 C 19941108; EP 0463104 A1 19920102; EP 0463104 A4 19930113; JP H04504340 A 19920730; KR 920700497 A 19920219; KR 950000050 B1 19950107

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