Title (en)

Graphics systems, palettes and methods with combined video and shift clock control

Title (de)

Graphische Systeme, Paletten und Verfahren mit kombinierter Video- und Schiebetaktsignalsteuerung

Title (fr)

Systèmes graphiques, palettes et méthodes avec commande combinée de vidéo et d'horloge de décalage

Publication

EP 0463867 B1 19991027 (EN)

Application

EP 91305764 A 19910626

Priority

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- US 54542490 A 19900627
- US 54617290 A 19900627

Abstract (en)

[origin: EP0463867A2] An integrated circuit (4000) for use with a plurality of clock oscillators (4100). The integrated circuit (4000) has a semiconductor chip, function performing circuitry (4051,4021,4030) fabricated on the semiconductor chip and responsive to clock pulses (CLK0-3) provided thereto, and a semiconductor chip package having pins connected to the function performing circuitry (4051,4021,4030). The integrated circuit further has a register (4013) accessible via the pins for external entry of clock control information. A clock control circuit (4040) responsive to the clock control information entered in said register (4013) has inputs connected to pins for the clock oscillators. The function performing circuitry (4051,4021,4030) is connected to the clock control circuit so that clock pulses are provided to the function performing circuitry by the clock control circuit, in accordance with the clock control information entered in the register (4013). Other integrated circuits, palette devices, computer graphics systems, printer systems and methods are also disclosed. <IMAGE>

IPC 1-7

G06F 1/04; G09G 5/18

IPC 8 full level

G06T 1/60 (2006.01); **G09G 5/06** (2006.01); **G09G 5/36** (2006.01); **G09G 5/39** (2006.01)

CPC (source: EP)

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