

Title (en)
IMAGE INFORMATION CONTROL APPARATUS AND DISPLAY SYSTEM

Publication
EP 0464620 A3 19921223 (EN)

Application
EP 91110530 A 19910626

Priority
JP 17110290 A 19900627

Abstract (en)
[origin: EP0464620A2] An image information control apparatus includes a partial write detector having at least two types of memory units for detecting and storing addresses accessed to a VRAM in units of lines in a scanning direction, thereby repeating the detection and the storage at different cycles, a circuit for performing calculations to recognize partial write information from contents of each of the memory units, memory units for storing the respective calculation results, a circuit for comparing the memory contents to determine a size relationship between partial write areas, a partial write ID signal controller for controlling a partial write ID signal on the basis of the size relationship between partial write areas and externally outputting the signal, and a circuit for, even when partial writing is being executed, forcibly interrupting the partial writing in accordance with a state of an external refresh control signal, starting refresh, and restarting the partial writing in accordance with a partial write state and a change in state of the refresh control signal. <IMAGE>

IPC 1-7
G09G 3/36

IPC 8 full level
G02F 1/133 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP KR)
G09G 3/00 (2013.01 - KR); **G09G 3/3629** (2013.01 - EP); **G09G 3/3651** (2013.01 - EP); **G09G 2310/04** (2013.01 - EP)

Citation (search report)
• [A] EP 0368117 A2 19900516 - CANON KK [JP]
• [A] EP 0361471 A2 19900404 - CANON KK [JP]

Cited by
AU672648B2; CN102737589A; EP0573822A1; US5929831A; EP0558342A1; US5717420A; EP0573821A1; US5926159A; EP0608053A3; US5844532A; EP0608056A1; US5576731A

Designated contracting state (EPC)
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