

Title (en)

Circuit generating driving signals for display.

Title (de)

Schaltungsanordnung zur Erzeugung von Anzeige-Treibersignalen.

Title (fr)

Circuit de génération de signaux de commande pour affichage.

Publication

EP 0464807 B1 19951018 (EN)

Application

EP 91111067 A 19910704

Priority

IT 6750190 A 19900706

Abstract (en)

[origin: EP0464807A2] A circuit generating signal for a matrix display of elements, in particular a display employing ferroelectric elements, the characterising principle of which consists in the fact that the generator comprises a digital memory (R) for a multiplicity of signals apt to build up a driving signal of a predetermined form and a selection device (C) of said signals. <IMAGE>

IPC 1-7

G09G 3/36

IPC 8 full level

G09G 3/36 (2006.01)

CPC (source: EP)

G09G 3/3629 (2013.01); **G09G 3/3651** (2013.01); **G09G 2310/06** (2013.01)

Cited by

EP0809231A3; EP0809232A3; US6057821A; US6215533B1

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EP 0464807 A2 19920108; EP 0464807 A3 19920701; EP 0464807 B1 19951018; DE 69113879 D1 19951123; DE 69113879 T2 19960411; ES 2081394 T3 19960301; IT 1240381 B 19931210; IT 9067501 A0 19900706; IT 9067501 A1 19920106

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