

Title (en)
MONITOR CONTROL CIRCUIT

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Application
EP 90904821 A 19900321

Priority
• DE 3915562 A 19890512
• EP 9000466 W 19900321

Abstract (en)
[origin: DE3915562C1] A monitor control circuit for driving a monitor which operates at a second pixel frequency on the basis of a digital image signal with a first pixel frequency has a first storage device (3) into which the image signal can be read with a frequency dependent on the first pixel frequency by means of a first control device, and a video storage device in effective connection with the output of the first storage device (3). To ensure constant updating of the image signal to be displayed, the first storage device is a first in-first out storage device (3), and a second storage device (6) connected to the video storage device (4) and to the first in-first out storage device (3) can read data words from the first in-first out storage device (3) and can write them into the video storage device (4) in such a manner that the reading of the data words from the first in-first out storage device (3) is interrupted when data words are being read from the video storage device (4). As a result, the number of data words stored in the first in-first out device (3) which can be re-stored in the video storage device (4) can vary.

IPC 1-7
G09G 1/16

IPC 8 full level
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