

Title (en)  
MONITOR CONTROL CIRCUIT

Title (de)  
MONITORSTEUERSCHALTUNG

Title (fr)  
CIRCUIT DE COMMANDE DE MONITEUR

Publication  
**EP 0468973 B2 20010509 (DE)**

Application  
**EP 90904821 A 19900321**

Priority  
• DE 3915562 A 19890512  
• EP 9000466 W 19900321

Abstract (en)  
[origin: EP0500147A2] A monitor control circuit serves to control a monitor whose display can be generated by reading out a digital video signal with a second pixel frequency from a video storage device, on the basis of a digital video signal having a first pixel frequency. For gap-free conversion of the first video signal to the second video signal, or for combining video signals of different graphics standards, the digital video signal of the first pixel frequency is read into a FIFO storage device (3) with a frequency dependent on the first pixel frequency and the data words of the digital video signal which are to be stored in the video storage device (4) are read out from the FIFO storage device (3) only during time segments in which no data are read out from the video storage device (4), whereby the number of data words which can be read out from the FIFO storage device (3) for storage in the video storage device (4) may vary. <IMAGE>

IPC 1-7  
**G09G 1/16**

IPC 8 full level  
**G09G 1/16** (2006.01); **G09G 5/12** (2006.01); **G09G 5/18** (2006.01); **G09G 5/393** (2006.01); **G09G 5/395** (2006.01)

CPC (source: EP KR US)  
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