

Title (en)
STABILIZING POWER SOURCE APPARATUS

Publication
EP 0471421 A3 19930203 (EN)

Application
EP 91202705 A 19851230

Priority
• EP 85309536 A 19851230
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Abstract (en)
[origin: EP0189680A2] A stabilizing power source apparatus includes a transformer (T_{3}) with one primary (N_p) and two secondary coils (N_{s1}), a switching transistor (Q_1), first and second differential amplifiers (A_1 , $A_{2'}$), an oscillator (OSC'), and a pulse modulator (PM). The oscillator generates a triangular wave of variable frequency. The modulator changes the pulse width and frequency of its output in accordance with a shift of a crossing point between the leading ramp of the triangular wave and the signal from the first differential amplifier (A_1). The pulse drives the switching transistor (Q_1), so that the duty factor of the transistor stabilizes a DC output voltage and a switching frequency thereof stabilizes an AC output voltage. Another stabilizing power source apparatus includes a saturable reactor (L_{s1} , L_{s2}) for stabilizing the DC output voltage and a resonance type inverter (L_p , $O_{2'}$, Q_3 , T_1 , N_{P1} , N_{P2} , $N_{D'}$, C_p) to generate a sinusoidal wave voltage.

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H02M 3/28; **H02M 7/48**

IPC 8 full level
H02M 3/335 (2006.01); **H02M 3/338** (2006.01)

CPC (source: EP US)
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Citation (search report)
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• [Y] US 3777248 A 19731204 - VERMOLEN J
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EP 0189680 A2 19860806; **EP 0189680 A3 19880316**; **EP 0189680 B1 19930210**; DE 3587090 D1 19930325; DE 3587090 T2 19930603; DE 3588108 D1 19960627; DE 3588108 T2 19961107; EP 0471421 A2 19920219; EP 0471421 A3 19930203; EP 0471421 B1 19960522; US 4677534 A 19870630

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