Title (en)

STABILIZING POWER SOURCE APPARATUS

Publication

EP 0471421 A3 19930203 (EN)

Application

EP 91202705 A 19851230

Priority

- EP 85309536 A 19851230
- JP 28092284 A 19841229
- JP 27771784 A 19841228

Abstract (en)

[origin: EP0189680A2] A stabilizing power source apparatus includes a transformer (T₃) with one primary (Np) and two secondary coils (N_s), a switching transistor (Q₁), first and second differential amplifiers (A,, A₂), an oscillator (OSC'), and a pulse modulator (PM). The oscillator generates a triangular wave of variable frequency. The modulator changes the pulse width and frequency of its output in accordance with a shift of a crossing point between the leading ramp of the triangular wave and the signal from the first differential amplifier (A,). The pulse drives the switching transistor (Q,), so that the duty factor of the transistor stabilizes a DC output voltage and a switching frequency thereof stabilizes an AC output voltage. Another stabilizing power source apparatus includes a saturable reactor (L_{s1}, L_{S2}) for stabilizing the DC output voltage and a resonance type inverter (Lp, O₂, Q₃, T,, N_{P1}, N_{P2}, N_D, Cp) to generate a sinusoidal wave voltage.

IPC 1-7

H02M 3/28; H02M 7/48

IPC 8 full level

H02M 3/335 (2006.01); **H02M 3/338** (2006.01)

CPC (source: EP US)

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Citation (search report)

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