

Title (en)

Driver protection circuit for a liquid crystal display.

Title (de)

Schutzschaltung für eine Flüssigkristallanzeigetreiberschaltung.

Title (fr)

Circuit de protection pour circuit de commande, notamment d'écran d'affichage à cristal liquide.

Publication

EP 0477099 B1 19941130 (FR)

Application

EP 91402494 A 19910919

Priority

FR 9011681 A 19900921

Abstract (en)

[origin: EP0477099A1] The protection circuit comprises three transistors (T1, T2, T3), permitting transfer of the voltage onto the output terminal (S), zero resetting of the midpoint (m) and zero resetting of the output. <??>Application to the control of liquid crystal display screens. <IMAGE>

IPC 1-7

G09G 3/36

IPC 8 full level

G02F 1/133 (2006.01); **G09G 3/36** (2006.01); **G09G 3/20** (2006.01)

CPC (source: EP US)

G09G 3/3688 (2013.01 - EP US); **G09G 3/2011** (2013.01 - EP US); **G09G 2310/0248** (2013.01 - EP US)

Cited by

EP0644523A3; US5801673A; EP0863498A3; CN111380739A

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

EP 0477099 A1 19920325; **EP 0477099 B1 19941130**; DE 69105431 D1 19950112; DE 69105431 T2 19950614; FR 2667187 A1 19920327; FR 2667187 B1 19950512; JP H05150216 A 19930618; US 5289332 A 19940222

DOCDB simple family (application)

EP 91402494 A 19910919; DE 69105431 T 19910919; FR 9011681 A 19900921; JP 26845991 A 19910919; US 76421191 A 19910923